

59th Allerton Conference

Scheduled for Sept 27-Sept 29, 2023

ON COMMUNICATION, CONTROL, & COMPUTING



Call for Papers: Due July 10, 2023 being firm

Manuscripts can be submitted from June 8-July 10, 2023 with the submission deadline of July 10th being firm. Please follow the instructions at: allerton.csl.illinois.edu.

CONFERENCE CO-CHAIRS | Max Raginsky **AND** Subhonmesh Bose
INFORMATION FOR AUTHORS | Regular papers suitable for presentation in 20 minutes are solicited. Regular papers will be published in full (subject to a maximum length of eight 8.5" x 11" pages, in two column format) in the Conference Proceedings. Only papers that are actually presented at the conference and uploaded as final manuscripts can be included in the proceedings, which will be available after the conference on IEEE Xplore. For reviewing purposes of papers, a title and a five to ten page extended abstract, including references and sufficient detail to permit careful reviewing, are required.

JULY 10 — Submission Deadline

AUGUST 7 — Author Notification Authors will be notified of acceptance via email by August 7, 2023, at which time they will also be sent detailed instructions for the preparation of their papers for the Conference Proceedings.

AFTER AUGUST 11 — Registration Opens

SEPTEMBER 27 – SEPTEMBER 29 — Conference Dates

SEPTEMBER 26 — Opening Tutorial Lectures: 8 am-5:30 pm | CSL BO2
Welcome Reception: 6 pm-8 pm | ECE 3002

SEPTEMBER 27 – September 29 — Conference Sessions at the University of Illinois Allerton Park & Retreat Center. The Allerton House is located 26 miles southwest of the Urbana-Champaign campus of the University of Illinois in a wooded area on the Sangamon River. It is part of the 1,500 acre Robert Allerton Park, a complex of natural and man-made beauty designated as a National natural landmark. Allerton Park has 20 miles of well-maintained trails and a living gallery of formal gardens, studded with sculptures collected from around the world.

TUTORIAL SPEAKERS:

Professor Victoria Kostina,

Electrical Engineering and Computing, CalTech, Pasadena, CA

Professor Alex Olshevsky, *Department of Electrical and Computer Engineering, Boston University, Boston, MA*

PLENARY SPEAKER:

Jitendra Malik, *Arthur J. Chick Professor of Electrical Engineering and Computer Sciences, University of California at Berkeley*

OCTOBER 3 — Final Paper Deadline Final versions of papers that are presented at the conference must be submitted electronically in order to appear in the Conference Proceedings and IEEE Xplore.

*The Allerton Conference is co-sponsored by the **Coordinated Science Lab** and the **Department of Electrical and Computer Engineering**.*

PAPERS PRESENTING ORIGINAL RESEARCH ARE SOLICITED IN THE AREAS OF:

- Biological Information Systems
- Coding Techniques and Applications
- Coding Theory
- Data Storage
- Information Theory
- Multiuser Detection and Estimation
- Network Information Theory
- Sensor Networks in Communications
- Wireless Communication Systems
- Intrusion/Anomaly Detection and Diagnosis
- Network Coding
- Network Games and Algorithms
- Performance Analysis
- Pricing and Congestion Control
- Reliability, Security and Trust
- Decentralized Control Systems
- Robust and Nonlinear Control
- Adaptive Control and Automation
- Robotics
- Distributed and Large-Scale Systems
- Complex Networked Systems
- Optimization
- Dynamic Games
- Machine Learning and Learning Theory
- Signal Models and Representations
- Signal Acquisition, Coding, and Retrieval
- Detection and Estimation
- Learning and Inference
- Statistical Signal Processing
- Sensor Networks
- Data Analytics
- Power System Control and Optimization

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